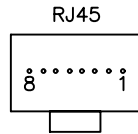
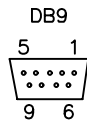


MAIN PORT (IN)



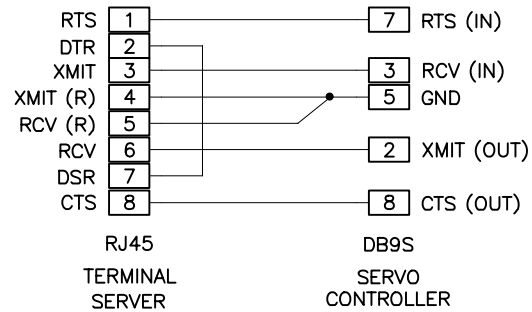
TERMINAL SERVER
PIN ASSIGNMENTS

PIN	SIGNAL	DIRECTION
1	RTS	OUT
2	DTR	OUT
3	TX+	OUT
4	TX-	OUT
5	RX-	IN
6	RX+	IN
7	DSR	IN
8	CTS	IN



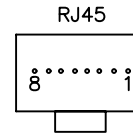
SERVO CONTROLLER
PIN ASSIGNMENTS

PIN	SIGNAL	DIRECTION
1	CTS	OUT
2	TRANSMIT	OUT
3	RECEIVE	IN
4	RTS	IN
5	GND	
6	CTS	OUT
7	RTS	IN
8	CTS	OUT
9	-	



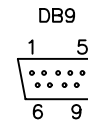
SERIAL CABLE TO SERVO CONTROLLERS

AUX PORT (OUT)



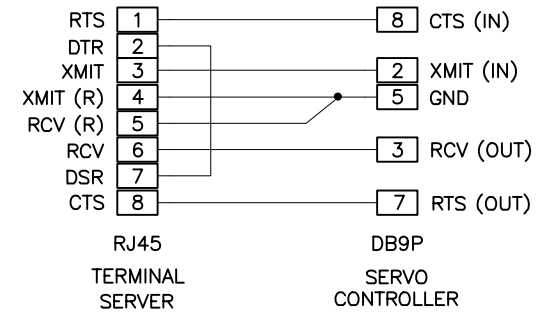
TERMINAL SERVER
PIN ASSIGNMENTS

PIN	SIGNAL	DIRECTION
1	RTS	OUT
2	DTR	OUT
3	TX+	OUT
4	TX-	OUT
5	RX-	IN
6	RX+	IN
7	DSR	IN
8	CTS	IN



SERVO CONTROLLER
PIN ASSIGNMENTS

PIN	SIGNAL	DIRECTION
1	CTS	IN
2	TRANSMIT	IN
3	RECEIVE	OUT
4	RTS	OUT
5	GND	
6	CTS	IN
7	RTS	OUT
8	CTS	IN
9	-	



SERIAL CABLE TO SERVO CONTROLLERS

REVISION

9-26-97 UPDATED PIN NUMBERS

UNIVERSITY OF CALIFORNIA
LICK OBSERVATORY

SERIAL CABLE WIRING
ESI SPECTROGRAPH

DES'N BY: B. Alcott

ORIGIN DATE: 4/17/97

DWG. NO.

NUM. OF 1

DRAWN BY:

MODIFY DATE: 9-26-97

PATH: ESI\SERCABLE

REV. A

EL-3311-1W