

MAIN (J2)

PIN	FUNCTION	ASSIGNMENT	PIN	FUNCTION	ASSIGNMENT
1	GND		31	MOT CMD W	LOW DISP. FLAT } SIGNALS TO SERVO AMPS
2	+5V		32	AMP EN W	
3	ERROR		33	A+ X	POST DISP. PRISM }
4	RESET		34	A- X	
5	SWITCH COM.		35	B+ X	
6	FWD LIM X	POST DISP. PRISM }	36	B- X	
7	REV LIM X				
8	HOME X	SPARE }	37	I+ X	MOTOR ENCODER SIGNALS }
9	FWD LIM Y				
10	REV LIM Y	IMAGE FLAT }	38	I- X	
11	HOME Y				
12	FWD LIM Z	LIMIT SWITCH SIGNALS }	39	A+ Y	
13	REV LIM Z				
14	HOME Z	LOW DISP. FLAT }	40	A- Y	
15	FWD LIM W				
16	REV LIM W	IMAGE FLAT }	41	B+ Y	
17	HOME W				
18	OUTPUT 1	SPARE }	42	B- Y	
19	INPUT COM.				
20	INPUT 1	LOW DISP. FLAT }	43	I+ Y	
21	INPUT 2				
22	INPUT 3	IMAGE FLAT }	44	I- Y	
23	INPUT 4				
24	ABORT	LOW DISP. FLAT }	45	A+ Z	
25	MOT CMD X				
26	AMP EN X	POST DISP. PRISM }	46	A- Z	
27	MOT CMD Y				
28	AMP EN Y	SPARE }	47	B+ Z	
29	MOT CMD Z				
30	AMP EN Z	IMAGE FLAT }	48	B- Z	
		SIGNALS TO SERVO AMPS }	49	I+ Z	
		LOW DISP. FLAT }	50	I- Z	
		POST DISP. PRISM }	51	A+ W	
		SPARE }	52	A- W	
		IMAGE FLAT }	53	B+ W	
		POST DISP. PRISM }	54	B- W	
		SPARE }	55	I+ W	
		IMAGE FLAT }	56	I- W	
		SIGNALS TO SERVO AMPS }	57	+12V	
		LOW DISP. FLAT }	58	-12V	
		POST DISP. PRISM }	59	+5V	
		SPARE }	60	GND	

GENERAL I/O (J5)

PIN	FUNCTION	ASSIGNMENT
1	ANALOG 1	-
2	ANALOG 2	-
3	ANALOG 3	-
4	ANALOG 4	-
5	ANALOG 5	-
6	ANALOG 6	-
7	ANALOG 7	-
8	GND	
9	+5V	
10	OUTPUT 1	POST DISP. PRISM }
11	OUTPUT 2	SPARE }
12	OUTPUT 3	IMAGE FLAT }
13	OUTPUT 4	LOW DISP. FLAT }
14	OUTPUT 5	-
15	OUTPUT 6	-
16	OUTPUT 7	-
17	OUTPUT 8	-
18	INPUT 8	-
19	INPUT 7	-
20	INPUT 6	-
21	INPUT 5	-
22	INPUT 4	LOW DISP. FLAT }
23	INPUT 3	IMAGE FLAT }
24	INPUT 2	SPARE }
25	INPUT 1	POST DISP. PRISM }
26	INPUT COM.	

AUXILIARY ENCODER (J3)

PIN	FUNCTION	ASSIGNMENT
1	SAMPLE CLK	
2	RESERVED	
3	B- W	B- }
4	B+ W	B+ }
5	A- W	A- }
6	A+ W	A+ }
7	B- Z	B- }
8	B+ Z	B+ }
9	A- Z	A- }
10	A+ Z	A+ }
11	B- Y	B- }
12	B+ Y	B+ }
13	A- Y	A- }
14	A+ Y	A+ }
15	B- X	B- }
16	B+ X	B+ }
17	A- X	A- }
18	A+ X	A+ }
19	+5V	
20	GND	

NOTE: THOUGH NOTED AS X, Y, Z, AND W-AXIS WE HAVE DESIGNATED THESE AS A, B, C, AND D-AXIS

UNIVERSITY OF CALIFORNIA  
LICK OBSERVATORY

SIGNAL ASSIGNMENTS  
CONTROLLER #1, 1ST BANK  
ESI SPECTROGRAPH

REVISION

11/19/97 ADDED NOTE  
1-15-98 ADDED IMAGE AND LOW DISPERSION FLAT STAGES  
12-8-98 REMOVED LAMP STAGE REFERENCES

DES'N BY: B. Alcott

ORIGIN DATE: 2/19/96

DWG. NO. NUM. 1 OF 1

DRAWN BY:

MODIFY DATE: 12-8-98

PATH: ESI\SIGNALS3

REV. A

EL-3322-1D