



EL-3194 JUMPERS

BIAS VOLTAGES		CLOCKS	
JUMPER	PIN	JUMPER	PIN
JP6	1,2	JP17	2,3
JP7	1,2	JP18	1,2
JP8	1,2	JP19	1,2
JP9	1,2	JP20	1,2
JP14	1,2		
JP15	2,3		
JP16	2,3		
JP21	2,3		

MITLL SINGLE AMPLIFIER READOUT

REVISION

UNIVERSITY OF CALIFORNIA LICK OBSERVATORY	
SDSU 2 CCD CONTROLLER CONFIGURATION	
DWG. NO.	EL-3177-3D
DES'N BY: B. Alcott	ORIGIN DATE: 10-22-97
DRAWN BY:	MODIFY DATE:
PATH: DEIMOS\SDSU2\CONFIGURE	SHEET 3 OF 7

REV A